

In re Patent Application of:

**CONTI**

Serial No. 10/606,189

Filing Date: June 25, 2003

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**In the Specification:**

Please replace the paragraph beginning at page 2, line 23, with the following rewritten paragraph:

According to one general characteristic of the invention, the switching means may comprise a control module for each radio-frequency channel. Each control module may comprise a PIN diode whose cathode is connected to the input/output terminal, and a control transistor whose base is connected to an input control that receives the switching control signal. Furthermore, the ~~sink (collector)~~ collector of this control transistor may be connected to the anode of the PIN diode for forming the common node between the PIN anode ~~intersections~~ junctions. According to a preferred embodiment, the control transistor comprises a lateral PNP transistor.

Please replace the paragraph beginning at page 6, line 2, with the following rewritten paragraph:

Apart from this diode DPN1, the control module that is on the RXGSM channel comprises a control transistor Q1, which in this case is a PNP type lateral transistor. The transistor Q1 ~~sink (collector)~~ collector is connected to the anode of the diode DPN1 via a shock inductor L1, whose function is to allow the DC current to pass and to resist the high frequency current. The transistor Q1 ~~sink~~ collector is furthermore connected to ground via another capacitor.

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Please replace the paragraph beginning at page 6, line 16, with the following rewritten paragraph:

The other control modules, respectively connected to the other radio-frequency channels, are identical to that which has just been described. Apart from the switching modules, the CM module also comprises an NPN transistor referenced Q6 whose ~~sink~~ collector is connected to the antenna and whose ~~transmitter~~ ~~(emitter)~~ emitter is connected to ground. The base of this transistor Q6 is connected to another control input EC6 that receives a transmission/reception switching signal. The transistor Q6 operates as a current source to limit the current to about 300 mA in the reception mode, and at about 4 mA in the transmission mode, for example.

Please replace the paragraph beginning at page 8, line 17, with the following rewritten paragraph:

The invention addresses this problem by combining with the PIN diode a control transistor whose ~~sink~~ collector is connected to the anode of this PIN diode. The ~~sink~~ collector is seen as forming the common node between the two PIN ~~intersection~~ junction anodes. One approach for creating such a transistor is to use a PNP lateral transistor as shown in Figure 2.

Please replace the paragraph beginning at page 8, line 31, with the following rewritten paragraph:

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The ~~sink (collector)~~ collector and ~~transmitter~~ ~~(emitter)~~ emitter regions, the P<sup>+</sup> doped regions, and the corresponding contacts are also housed inside the CS casing. Electrically, the equivalent diagram of this lateral transistor Q1 is illustrated in Figure 3. In this figure, the transistor Q1 designates the lateral transistor, whereas the transistors Q20 and Q30 represent PNP type parasitic transistors.

Please replace the paragraph beginning at page 9, line 6, with the following rewritten paragraph:

It can therefore be seen, by referring more particularly to Figure 4, that, in this case, the transistor Q1 ~~sink~~ collector is seen as forming the common node between the anode of the ~~intersection~~ junction of PN (diode) J1 formed by the ~~sink/base~~ collector/base of transistor Q1, and PN (diode) J30 of the parasitic transistor Q30. These various ~~intersections~~ junctions are also shown in Figure 5.